

FIG. 1

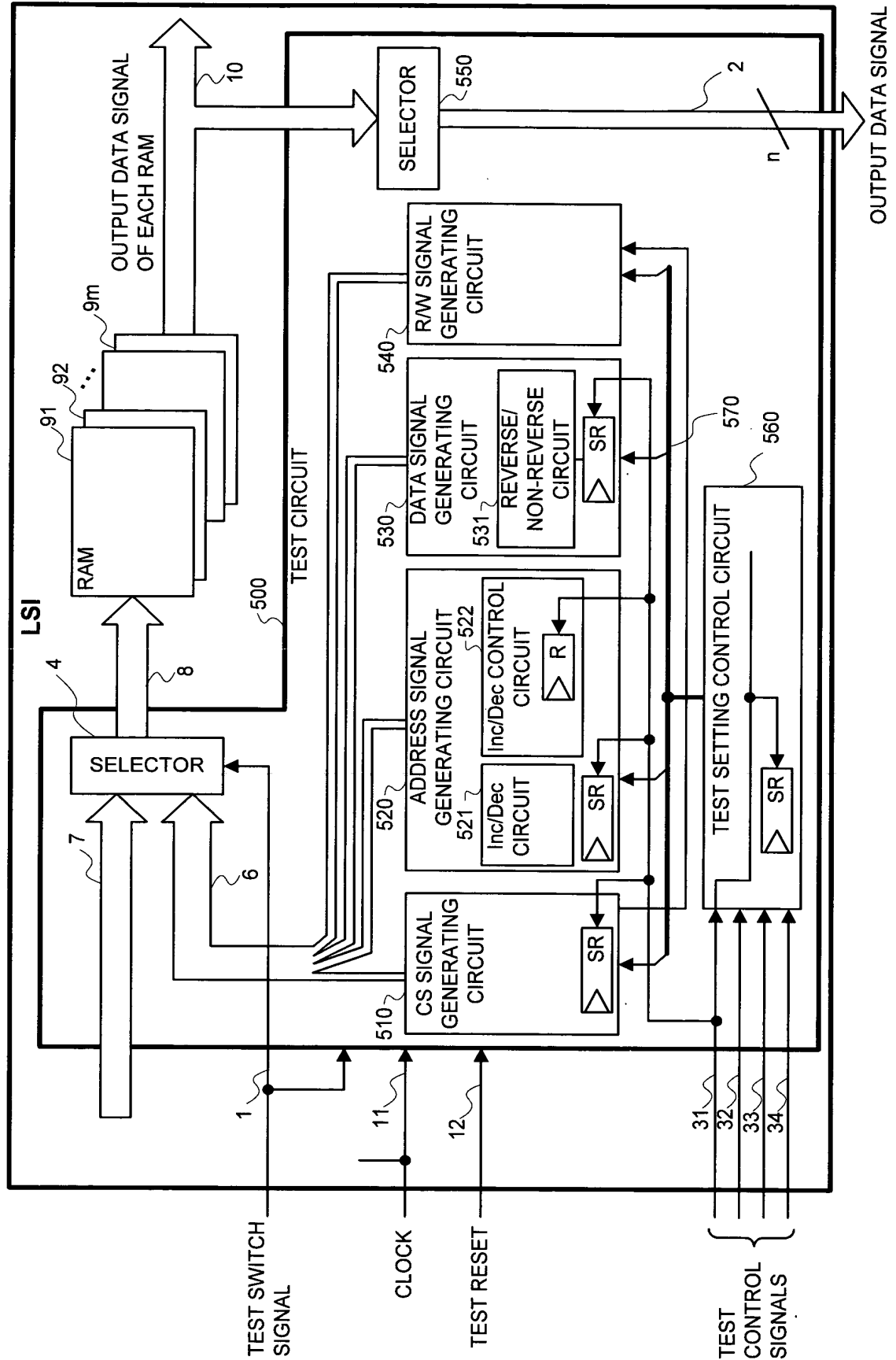


FIG. 2

CONTROL SIGNAL				OPERATION MODE	OPERATION CONTENTS
34	33	32	31		
0	0	0	0	TEST SETTING	NO OPERATION
	0	0	1		RESERVED
	0	1	x		SHIFT SR OF SIGNAL GENERATING CIRCUITS 510-530 AND Inc/Dec CONTROL CIRCUIT 522. INPUT IS CONTROL SIGNAL 31.
	1	0	x		SHIFT SR OF TEST SETTING CONTROL CIRCUIT 560. INPUT IS CONTROL SIGNAL 31.
1	1	1	x	TEST EXECUTION	RESERVED
	0/1	x	x		CONTROL OF PRESENCE OR ABSENCE OF ADDRESS Inc(Dec). 1: PRESENCE 0: ABSENCE
	x	0/1	x		CONTROL OF RW. 1: W(WRITE) 0: R(READ)
	x	x	0/1		CONTROL OF PRESENCE OR ABSENCE OF DATA REVERSE. 1: REVERSE 0: NON-REVERSE

FIG. 3

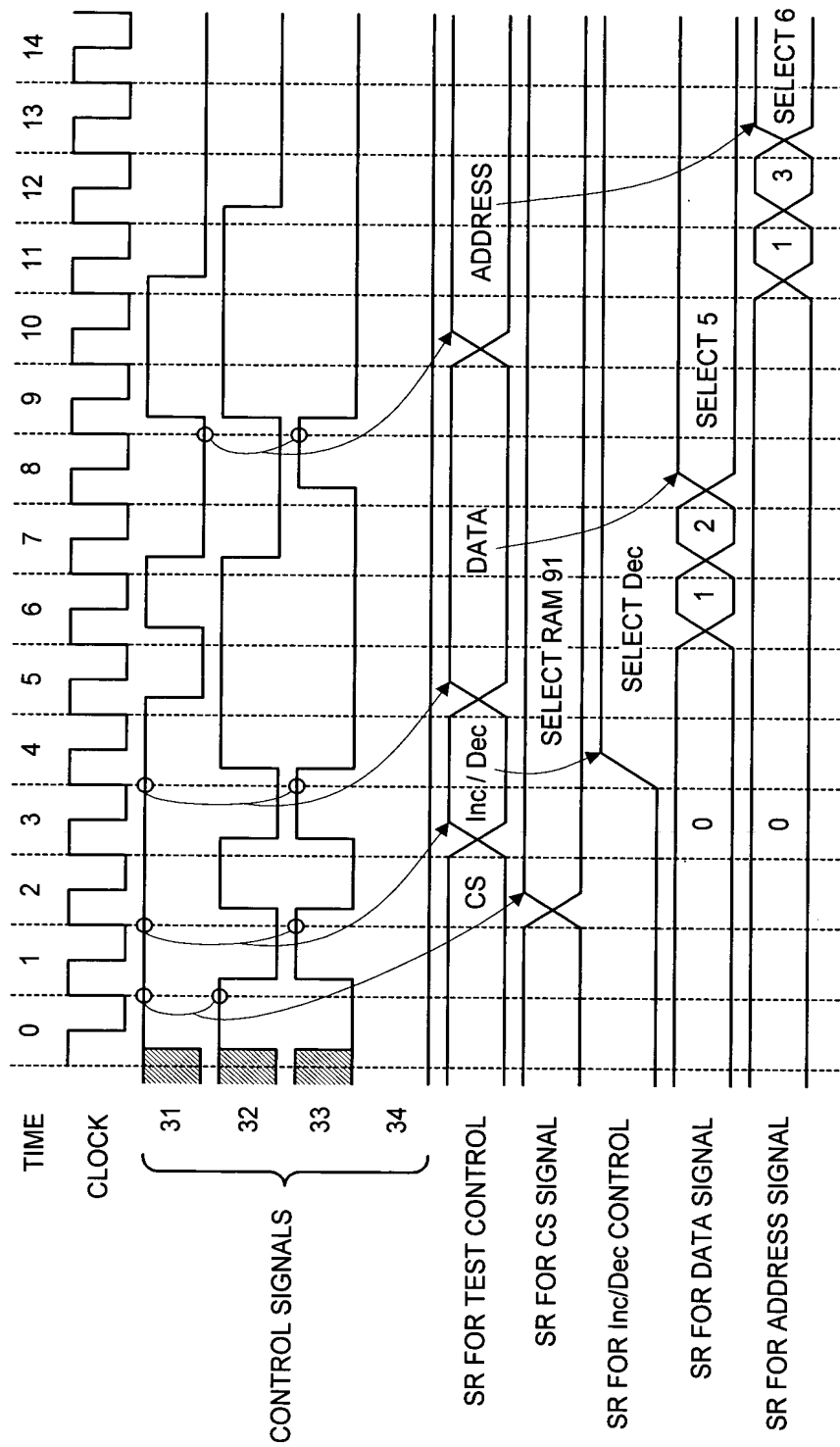


FIG. 5

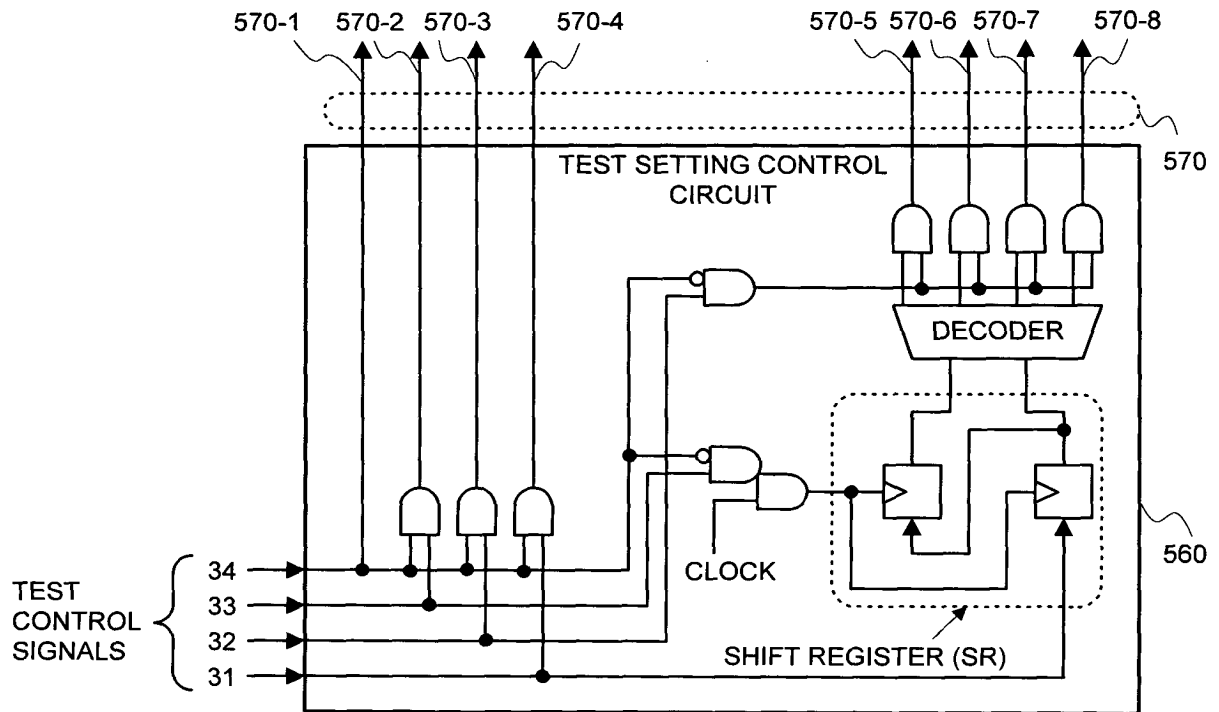


FIG. 6

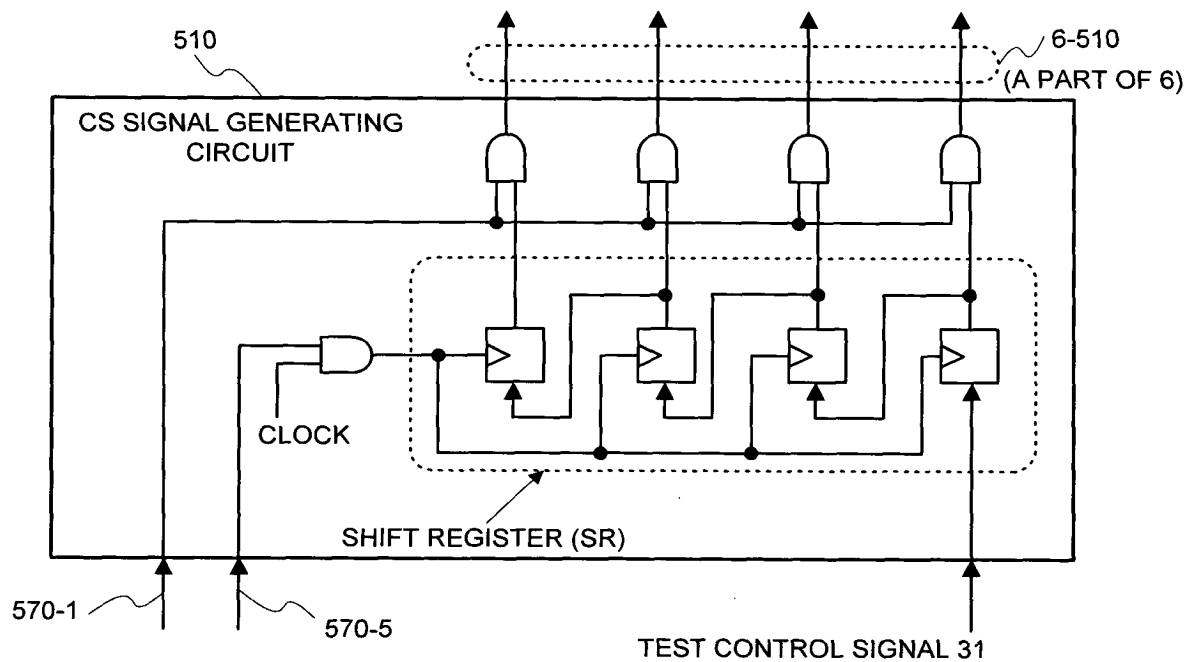


FIG. 7

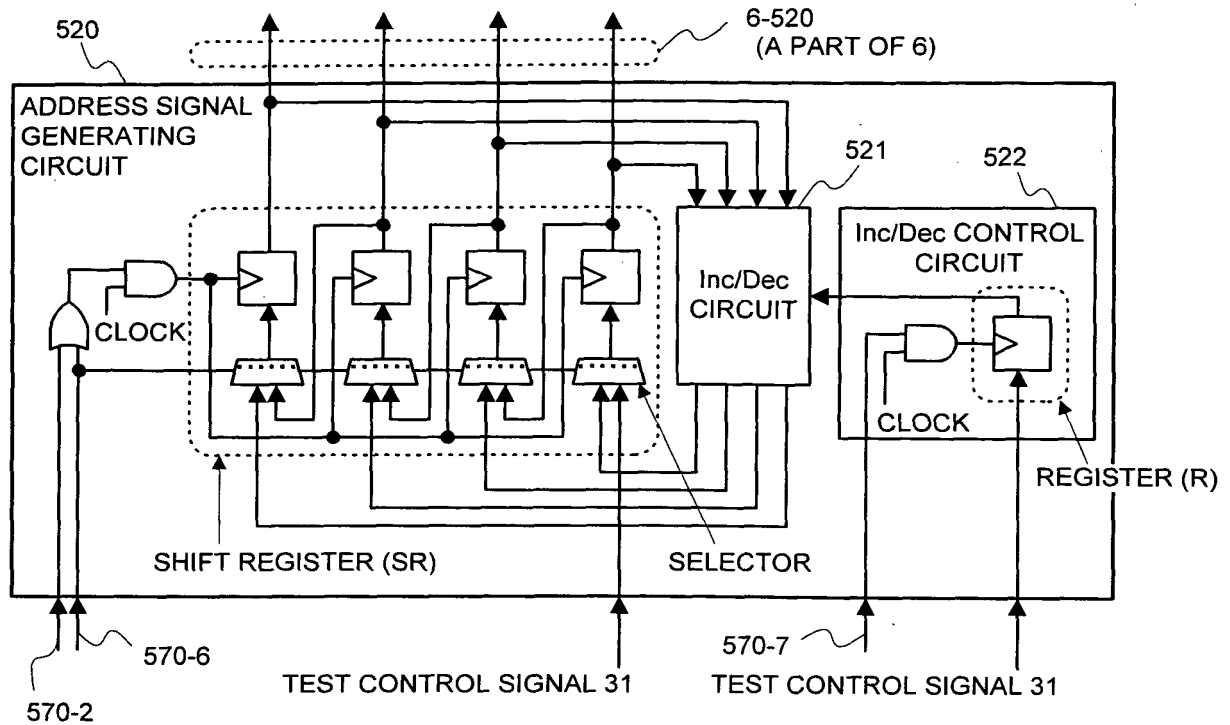


FIG. 8

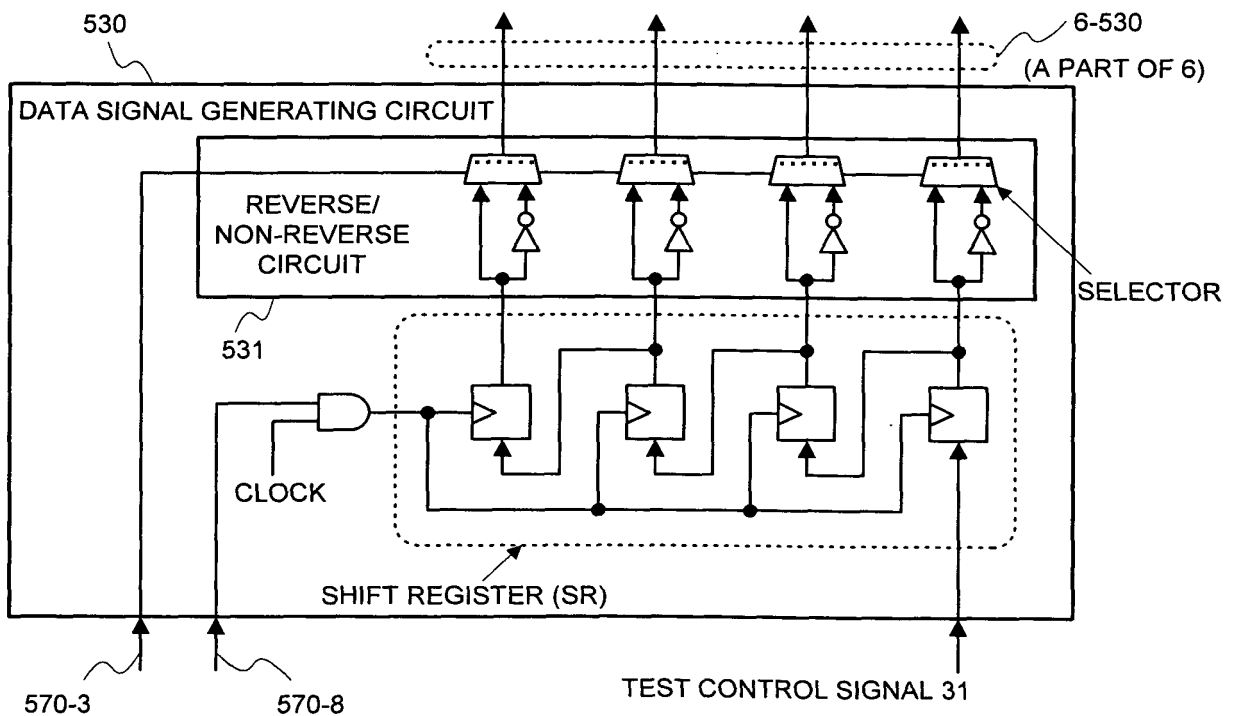


FIG. 9

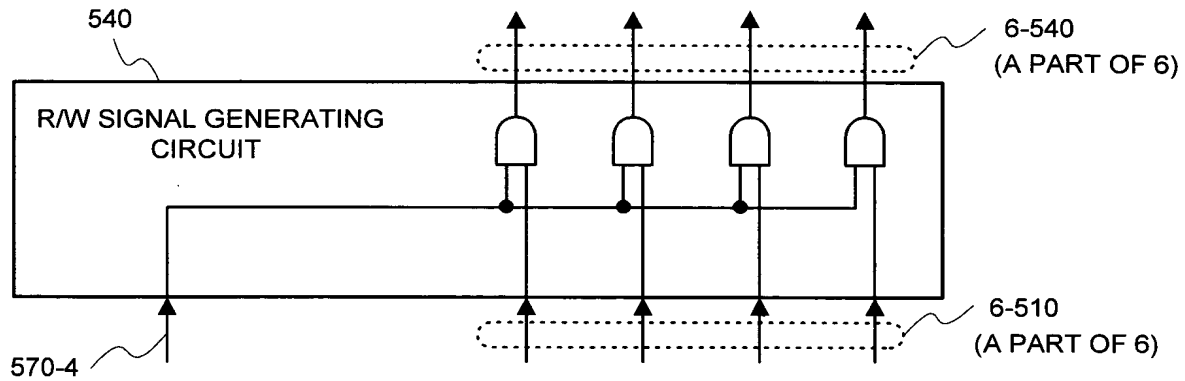


FIG. 10
PRIOR ART

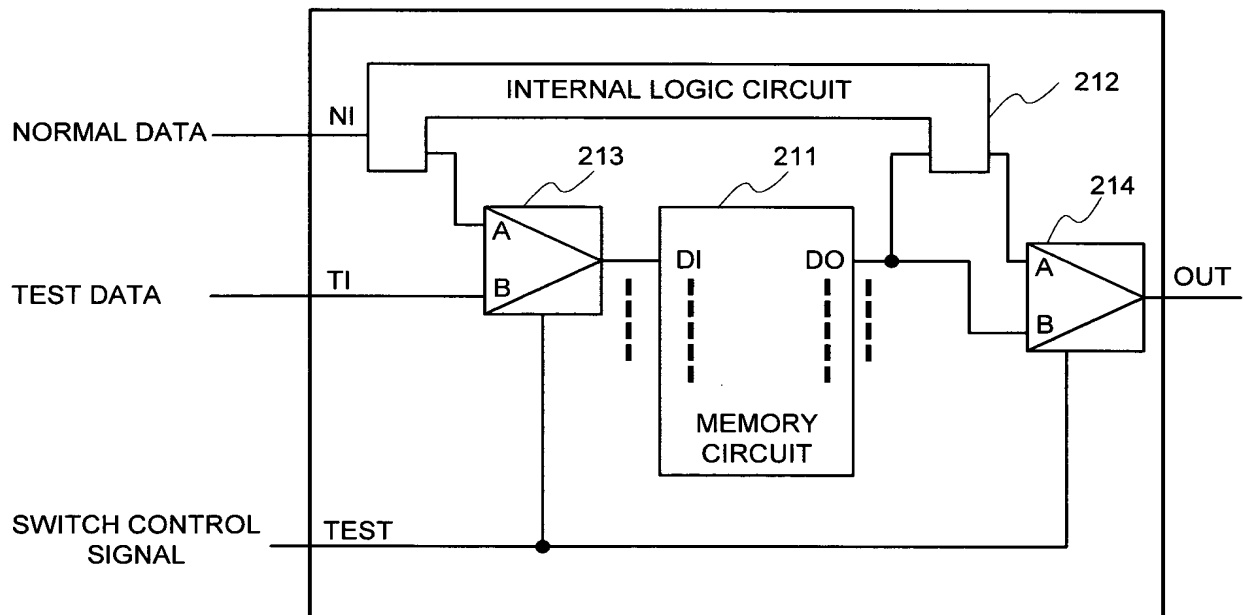


FIG. 11
PRIOR ART

